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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshinori Hino et al.

Art Unit : 2811

Serial No. : 10/080,831

Examiner : J. Nguyen

Filed : February 21, 2002

Title : SEMICONDUCTOR DEVICE AND PATTERN LAYOUT METHOD THEREOF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

REPLY


In reply to the action mailed April 25, 2003, please amend the application as indicated on the following pages. Applicant asks that all claims be allowed in view of the amendment to the claims and remarks contained on the following sheets, a total of 5 pages.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

June 25, 2003



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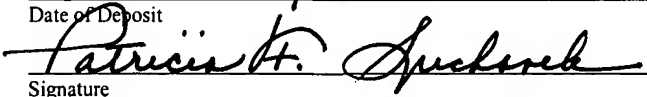
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